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L29	L28 and mailbox	9	L29
L28	((virtual adj machine) and (common adj memory) and (heterogeneous adj processors) and (instruction adj set))	14	L28
L27	((virtual adj machine) and (common adj memory) and (heterogeneous adj processors) and (instruction adj set))	14	L27
L26	L23 and ((virtual adj machine) and (common adj memory) and (heterogeneous adj processors) and (instruction adj set))	0	L26
L25	L23 and (virtual adj machine) and (common adj memory) and (heterogeneous adj processors) and (instruction adj set)	0	L25
L24	L23 and (virtual adj machine) and ((common adj memory) or (tightly adj coupled)) and (heterogeneous adj processor) and (instruction adj set)	0	L24
L23	(718/105).ccls. or 708/607.ccls. or 710/68.ccls. or 717/149,134,147.ccls.	520	L23
	<i>DB=USPT; PLUR=NO; OP=OR</i>		

<u>L22</u>	L20 AND (DMA or (direct ADJ memory ADJ access))	1	<u>L22</u>
<u>L21</u>	L20 (DMA or (direct ADJ memory ADJ access))	28890	<u>L21</u>
<u>L20</u>	L18 and heterogeneous	24	<u>L20</u>
<u>L19</u>	L18 and homogenous	0	<u>L19</u>
<u>L18</u>	L17 and (virtual adj machine)	86	<u>L18</u>
<u>L17</u>	L1 and (multimedia)	212	<u>L17</u>
<u>L16</u>	L15 and (virtual adj machine)	3	<u>L16</u>
<u>L15</u>	(708/607).ccls. or (710/68).ccls.	381	<u>L15</u>
<u>L14</u>	L13 or L12	53	<u>L14</u>
<u>L13</u>	L11 and ((tightly ADJ coupled) or (tightly-coupled))	34	<u>L13</u>
<u>L12</u>	L11 and JVM	19	<u>L12</u>
<u>L11</u>	(718/105).ccls.	775	<u>L11</u>
<u>L10</u>	L9 and (virtual ADJ machine)	4	<u>L10</u>
<u>L9</u>	L8 or L7	208	<u>L9</u>
<u>L8</u>	(tightly-coupled).ab.	20	<u>L8</u>
<u>L7</u>	(tightly ADJ coupled).ab.	208	<u>L7</u>
<u>L6</u>	L3 and DMA	3	<u>L6</u>
<u>L5</u>	L4 and data.ab.	18	<u>L5</u>
<u>L4</u>	L2 and (717/148).ccls.	75	<u>L4</u>
<u>L3</u>	L2 and 717/\$\$\$ccls.	245	<u>L3</u>
<u>L2</u>	L1 and JIT	419	<u>L2</u>
<u>L1</u>	JAVA and ((Just-in-time) Or (just ADJ in ADJ time) same (compile or compiler))	920	<u>L1</u>

END OF SEARCH HISTORY